



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: OH ET AL. DOCKET NO.: WK2K1070
SERIAL NO: 10/015,374 EXAMINER: WILLIAMS
FILED: 12/12/01 ART UNIT: 2826
TITLE: STACKING STRUCTURE OF SEMICONDUCTOR CHIPS AND SEMICONDUCTOR PACKAGE USING IT

Assistant Commissioner
for Patents
Washington, D.C. 20231

Weiss, Moy & Harris, PC
4202 N. Brown Avenue
Scottsdale, AZ, 85251

December 13, 2002

I hereby certify that on the 13th day of December, 2002, this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicant brings to the attention of the Examiner the documents listed on the attached form PTO-1449 listing the following references. Enclosed is a check in the amount of \$180.00 in accordance with 37 C.F.R. § 1.97(c) (2).

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The listed references may be relevant to the prosecution of the subject application. Applicant respectfully requests that these references be considered and made of record.

No representation is made or intended as to the completeness of this list, nor is this submission of references an admission that these references are prior art or material to the subject application.

If there are any fees incurred by this Amendment Letter, please deduct them from our Deposit Account NO. 23-0830.

Respectfully submitted,



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JDM/wp